REMARKS

The substitute Abstract contains no new matter. In order that the examiner can satisfy himself in this regard, also submitted herewith is a marked-up copy of the original Abstract from which the substitute Abstract was typed.

Respectfully submitted

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ABSTRACT OF THE DISCLOSURE

To provide a digital power amplifier having high power amplification efficiency.

present invention relates to which a pair of first and second switching amplifier in elements inserted, between a high potential power supply ine and a low potential power supply line is caused operate complementarily, and when Vswitching the switching element which has been switched on, dead time 10 is provided, to control power supply to a low-pass filter. In a switching section, the first switching element, a first coil, a second coil and the second switchingelement are connected in series, between the high potential power supply line and the low potential power The switching section comprises a first 15 high-speed diodevin which a cathode is connected to the high potential power supply line and an anode is connected to a node between the second coil and the second switching element, and a second high-speed diode in which 20 a cathode is connected to a node between the first switching element and the first coil, and an anode is connected to the low potential power supply line. node between the first coil and the second coil is connected to the low-pass filter side.